

Summary of Reference Implementations

Scheme	CLOC-Twine	SILC-Present	SILC-LED
Family	Cyclone V	Cyclone V	Cyclone V
Device	5CEFA4F23C7	5CEFA4F23C7	5CEFA4F23C7
ALMs	981	902	975
Registers	857	979	979
Fmax	174.49 (MHz)	166.61 (MHz)	142.31 (MHz)
Throughput	159.53 (Mbps)	166.61 (Mbps)	92.93 (Mbps)

- Reference implementations
- Compatible with CAESAR_API of ATHENa, GMU